



9100 Series

Digital Delay Generator

The next generation of compact pulse generators is here

OEMs and research labs looking for a compact but powerful timing device.

9100 and 8100 board level is your answer



Precision Timing in a Compact Form

Delivering high resolution with low jitter



Specifications

- Independent Outputs
- Resolution: 250ps
- Jitter: < 50 ps RMS (Channel to Channel)
- 20MHz Rep Rate
- Clock input and clock output signals
- Comm Port: USB
- Configurable output channel connections on request

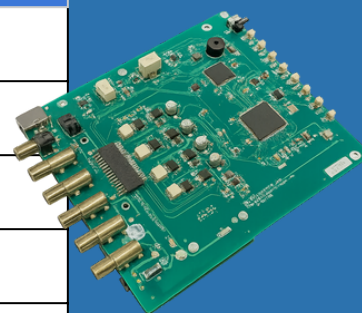


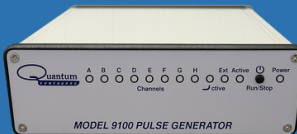
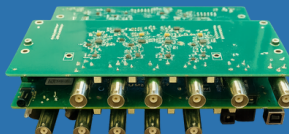
Features

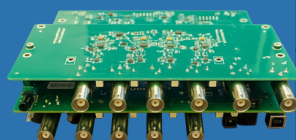
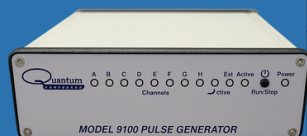
- Memory:
 - 6 Storage Slots
- Software GUI interface
- TTL Mode can be powered by USB 3.0 or higher port
- 2 year warranty
- Optional
 - TZ50 Impedance matching

Internal Rate Generator				
Rate (T0Period)	0.0002	-	20,000,000	Hz
Time Base	200MHz, Low Jitter PLL			
System Modes	Single, Continuous, Burst, Duty Cycle, External Gate/Trigger			
Burst Mode	1	-	4,000,000,000	Pulses
Duty Cycle Mode	1	-	4,000,000,000	Pulses
External Clock In/Out	Available on 8 Channel Units Only			

Channel Timing Generator				
Pulse Width Range	10n	-	2,000	s
Width Resolution	-	250	-	ps
Pulse Delay Range	0	-	2,000	s
Delay Resolution	-	250	-	ps
Jitter (Channel to Channel)	50ps + (.000000001 x delay) RMS			



  	Output Module				
	TTL/CMOS MODE				
	Output Impedance	-	50	-	Ohms
	Output Level	4.0 VDC into $\geq 1K$ ohm			
	Output Current	5mA typical into 1K ohm 50mA typical into 50 ohm			
	Rise Time (10% - 90%)	< 3ns typical into $\geq 1K$ ohm (10% - 90%)			
	ADJUSTABLE MODE				
Output Level	2.0 to 20 VDC into $\geq 1K$ ohm 1.0 to 10 VDC into ≥ 50 ohms				
Rise Time (10% - 90%)	15ns typical @ 20V (High Imp) 25ns typical @ 10V (50 ohm)				



Dimensions	9100: 165mm x 180mm x 55mm 8100: 160mm x 178mm x 26mm
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